

## **GUEST LECTURE ON “VLSI DESIGN”**

The Department of Physics & Electronics, K.B.N. College (Autonomous), Vijayawada organized a Guest Lecture on “VLSI Design” on 23rd February 2026 at 1:00 PM in the Vivekananda Seminar Hall. The objective of the lecture was to create awareness among students about modern opportunities and global trends in the semiconductor industry. The program was presided over by the respected Principal Dr. G. Krishnaveni. The invited resource person was Dr. M. Durga Prakash, Associate Professor, Department of Electronics and Communication Engineering, SRM University, Andhra Pradesh.

During the session, the speaker discussed modern opportunities in the semiconductor industry, global demand and industry acceleration, major semiconductor industries, key technologies in VLSI, and the semiconductor supply chain. He explained the rapid growth of the semiconductor industry driven by increasing demand in areas such as artificial intelligence, communication systems, automotive electronics, and consumer devices. The session also highlighted how various countries are investing significantly to strengthen their semiconductor ecosystems.

A key part of the lecture focused on the Semiconductor Supply Chain, including chip design, fabrication (manufacturing of wafers), packaging, and testing processes. The lecture was highly informative and interactive, with students actively participating and clarifying their queries.

The program was conducted under the guidance and support of Dr. G. Krishnaveni, Principal; Dr. T. Srinivasu, Secretary & Correspondent; Dr. V. Narayana Rao, Administrative Officer; and Sri R. Uday Kumar, Assistant Professor & HoD, Department of Physics & Electronics.

The event was coordinated by the faculty members Sri A.H.D. Prakash, Assistant Professor in Physics; Dr. M. Hemalatha, Assistant Professor in Physics; and Ms. P. Tanuja, Lecturer in Electronics, along with other members of the department.

Around 70 students from II B.Sc. (Electronics), III B.Sc. (Electronics), II B.Sc. (Physics), and III B.Sc. (Physics) from both Major and Minor streams enthusiastically participated in the guest lecture.

### **Outcomes:**

1. Students gained basic understanding of VLSI design concepts and semiconductor technologies.
2. Participants became aware of career opportunities in the semiconductor and VLSI industry.
3. The session helped students understand the global demand and growth of the semiconductor sector.
4. Students learned about the semiconductor supply chain, including design, fabrication, packaging, and testing processes.
5. The lecture encouraged students to explore higher studies and research in electronics and VLSI technologies.
6. It enhanced students' interest in emerging technologies such as AI hardware, communication systems, and embedded electronics.
7. The interaction with the resource person helped students clarify doubts and improve their technical knowledge.

## INVITATION



ISO 9001:2015

NAAC 'A++' GRADE CYCLE 4

**KAKARAPARTI BHAVANARAYANA COLLEGE**

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*Invitation*



MHRD'S  
INNOVATION CELL  
(GOVT. OF INDIA)

Guest Lecture on

# **VLSI DESIGN**

(Very Large Scale Integration)

**23<sup>rd</sup> Feb, 2026 @ 1:00 p.m.**

**Venue: Vivekananda Seminar Hall**



**Resource Person**

**Dr. M. Durga Prakash**

Associate Professor,  
Dept. of Electronics and  
Communication Engineering,  
SRM University.

**Presided by**

**Dr. G. Krishnaveni**

Principal, K.B.N. College

**Organized by**

**Department of Physics & Electronics**

## **PHOTO GALLERY**



**RESOURCE PERSON INTRODUCTION BY Sri. A.H.D PRAKASH**



**RESOURCE PERSON Dr. M. DURGA PRAKASH, DELIVERING THE TALK**



**PARTICIPANTS AT THE PROGRAMME**



**RESOURCE PERSON INTERACTING THE STUDENTS**

